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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/166,731 06/12/2002 PAT 6,680,441 *Yes mt*

** FOREIGN APPLICATIONS *****
 JAPAN 2001-179118 06/13/2001
 JAPAN 2001-199392 06/29/2001
 JAPAN 2001-204023 07/04/2001
 JAPAN 2002-62394 03/07/2002 *Yes mt*

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 22	INDEPENDENT CLAIMS 2	
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance					
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>			

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TITLE
 Method for manufacturing printed wiring board with embedded electric device

☐ All Fees